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Date:

February 13, 2008

To:

United States Patent and Trademark Office

Examiner: Chen, Kin Chan; Art Unit: 1792

Fax:

(571) 273-8300

Re:

Application Serial No.: 10/705,347

Filing Date: 11/8/2003; First-Named Inventor: Labelle

Attorney Docket No.: 0180151

From:

Farjami & Farjami LLP

Number of pages including the cover sheet: 4

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on 2/13/08	For: Method for Integrating a High-K Gate Dielectric in a Transistor Fabrication Process					
Signature Christina Carter Ellis	Art Unit 1792		Examiner CHEN, Kin Chan			
Typed or printed name Christina Carter Ellis						
Applicant hereby appeals to the Board of Patent Appeals and Interferences from the last decision of the examiner.						
The fee for this Notice of Appeal is (37 CFR 1.17(b))			\$ <u>510.00</u>			
Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee shown above is reduced by half, and the resulting fee is:						
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applicant/inventor.	15.65		Simulation			
assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)	Signature Michael Farjami, Esq.					
	Typed or printed name					
x attorney or agent of record. Registration number 38,135			282-1000 ephone number			
attorney or agent acting under 37 CFR 1.34(a). Registration number if acting under 37 CFR 1.34(a).			3/08/ Date			
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.						
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name Christina Carter Ellis					
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assignee of record of the entire interest.	*				
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